

UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,253,793 B2  
APPLICATION NO. : 10/625617  
DATED : August 7, 2007  
INVENTOR(S) : Tokuroh Ozawa and Mutsumi Kimura

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In claim 1,

Col. 28, line 37, change "plurality of power" to "plurality of common power".

In claim 2,

Col. 29, line 6, change "plurality of power" to "plurality of common power".

In claim 3,

Col. 29, line 18, change "first transistor and the second transistor" to "first transistors and the second transistors".

In claim 5,

Col. 29, line 41, change "plurality of power" to "plurality of common power".

In claim 6,

Col. 30, line 13, change "plurality of power" to "plurality of common power".

Delete claims 7 and 8.

In claim 9,

Col. 31, line 60, change "plurality of power" to "plurality of common power".

In claim 10,

Col. 32, line 31, change "plurality of power" to "plurality of common power".

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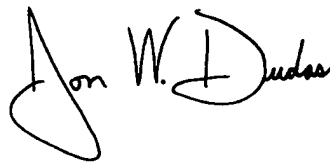
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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In claim 13,  
Col. 34, line 5, change "plurality of power" to "plurality of common power".

Signed and Sealed this

Second Day of December, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*